

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,874,136 B2  
APPLICATION NO. : 10/043964  
DATED : March 29, 2005  
INVENTOR(S) : Frederic Reblewski et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 5, line 24, "transistor; where" should read --transistor and wherein a gate of the first pass transistor is coupled to a first signal and a gate of the second pass transistor is coupled to a second signal; where--.

Column 5, line 44, "raise a voltage raised by" should read --raise a voltage by--.

Column 5, line 53, "wherein each the" should read --wherein each of the--.

Column 5, line 54, "pass transistors coupled" should read --pass transistor coupled--.

Column 5, line 58, "transistor; where" should read --transistor and wherein a gate of the first pass transistor is coupled to a first signal and a gate of the second pass transistor is coupled to a second signal; where--.

Column 5, line 65, "wherein each the" should read --wherein each of the--.

Column 6, line 3, "transistor; where" should read --transistor and wherein a gate of the first pass transistor is coupled to a first signal and a gate of the second pass transistor is coupled to a second signal; where--.

Column 6, line 7, "coupled a pass" should read --coupled to a pass--.

Column 6, line 13, "reconfigurable logic circuit" should read --reconfigurable circuit--.

Column 6, line 35, "wherein each the" should read --wherein each of the--.

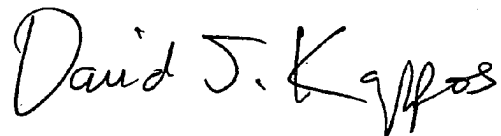
Column 6, line 40, "transistor; where" should read --transistor and wherein a gate of the first pass transistor is coupled to a first signal and a gate of the second pass transistor is coupled to a second signal; where--.

Column 6, line 53, "transistor; where" should read --transistor and wherein a gate of the first pass transistor is coupled to a first signal and a gate of the second pass transistor is coupled to a second signal; where--.

Column 8, line 2, "some know logic" should read --some known logic--.

Signed and Sealed this

Twenty-seventh Day of October, 2009



David J. Kappos  
*Director of the United States Patent and Trademark Office*